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- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

## Tables of Contents

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- ☐ Standards

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- ☐ By Author
- ☐ Basic
- ☐ Advanced

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1 **Content-based mesh generation algorithm**
*Konyha, L.; Enyedi, B.; Tran, S.M.; Fazekas, K.;*

Video/Image Processing and Multimedia Communications, 2003. 4th EURASIP Conference focused on , Volume: 1 , 2-5 July 2003

Pages:175 - 180 vol.1

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**Web** Results 1 - 10 of about 5,790 for **detect OR detecting phase frame OR boundary "ECC"**. (0.57 seconds)

### Digital Audio Broadcasting

... flash converters with <250picosecond windows, one to **detect** each **phase** ... signal to lock on to the **phase** clock of ... **ECC frame** size is 16 kilobits (8 kilosymbols, 8 ...

[www.mega.nu:8080/gallery/dab.html](http://www.mega.nu:8080/gallery/dab.html) - 14k - [Cached](#) - [Similar pages](#)

### CD Freaks.com - Why DVD+R(W) is superior to DVD-R(W) [1/1]

... period lasts 32T, which makes it much easier to **detect**. ... the pre-pits more error prone than **phase** modulation, but ... which gives at least one sync **frame** less to ...

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### [PDF] PowerPoint - THIC9704-MIC.ppt

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... High Speed Search User Data Area Previous Group written Total Group written Previous Group read Total Group read Total Rewritten **frame** Total 3rd **ECC** count Load ...

[www.thic.org/pdf/Apr97/sony.ytakayama.pdf](http://www.thic.org/pdf/Apr97/sony.ytakayama.pdf) - Aug 3, 2004 - [Similar pages](#)

### [PDF] Microsoft PowerPoint - PCI-SIG PCI-X 2.0 Presentation DRAFT H.ppt

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... Response **Phase** Bus Transaction Initiator Termination TRDY# IRDY# **FRAME#** BUS CMD C ... bit correction on all phases Supports nibble **detect** and **phase** error **detect** ...

[www.pcisig.com/specifications/pcix\\_20/pci\\_x\\_2dot0\\_presentation.pdf](http://www.pcisig.com/specifications/pcix_20/pci_x_2dot0_presentation.pdf) - [Similar pages](#)

### [PDF] Threshold Vision in Amblyopia: Orientation and **Phase**

File Format: PDF/Adobe Acrobat

... The mean screen luminance was 100 cd/m<sup>2</sup> and the **frame** rate was ... F IGURE 3. Comparison of the contrast needed to **detect** and to discriminate the **phase** (0° vs ...

[www.iovs.org/cgi/reprint/44/11/4762.pdf](http://www.iovs.org/cgi/reprint/44/11/4762.pdf) - [Similar pages](#)

### [PDF] DIGITAL SIGNAL PROCESSOR FEATURES ORDERING INFORMATION

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... SDAT 32 26 SBCK S0S1 EFM **PHASE** DETECTOR 23BIT ... SUBCODE-Q REGISTER DIGITAL PLL **FRAME** SYNC DETECTOR ... MODE SELECTOR DIGITAL OUTPUT 16K SRAM **ECC** INTERPOLATOR DIGITAL ...

[www.samsung.com/.../SystemLSI/DigitalMedia/OpticalASSP/OpticalPlayer/CDP/S5L9286F/s5l9286f01.pdf](http://www.samsung.com/.../SystemLSI/DigitalMedia/OpticalASSP/OpticalPlayer/CDP/S5L9286F/s5l9286f01.pdf) -

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### [PDF] Abstract

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... all stations going to a backoff **phase** before transmission ... In order to **detect** a collision, the entire ... **Frame** Space) • EIFS (Extended Inter **Frame** Space) SIFS and ...

[www.com.dtu.dk/research/networks/OPNET/DCF\\_MAC.pdf](http://www.com.dtu.dk/research/networks/OPNET/DCF_MAC.pdf) - [Similar pages](#)

### [PDF] 15-441 Computer Networking Links From Signals to Packets Link ...

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... On means 1, Off means 0 • Similar: frequency or **phase** modulation ... byte stuffing • Encoding • Add implied 0 to end of **frame** • Each 0 ... Can **detect** all burst ...

[www-2.cs.cmu.edu/afs/cs/academic/class/15441-f01/www/lectures/lecture04.PDF](http://www-2.cs.cmu.edu/afs/cs/academic/class/15441-f01/www/lectures/lecture04.PDF) - [Similar pages](#)

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